EVERSPIN®
TECHNOLOGIES

Investor Presentation

September 2026

Safe Harbor Statement

FORWARD LOOKING STATEMENTS

This presentation contains "forward-looking statements" that involve risks, uncertainties and assumptions. If the risks or uncertainties materialize or the assumptions prove incorrect, our results may differ materially from those expressed or implied by such forward-looking statements. All statements other than statements of historical fact could be deemed forward-looking statements, including, but not limited to: any estimates of addressable market size and our ability to capture that market, market trends and market opportunities, customer growth, product availability, technology developments, or other future events; any statements regarding our plans, strategies or objectives with respect to future operations or business performance; any statements regarding future economic conditions; and any statements of assumptions underlying any of the foregoing. These statements are based on estimates and information available to us at the time of this presentation and are not guarantees of future performance. Actual results could differ materially from our current expectations as a result of many factors, including, but not limited to, risks related to: market adoption of our products; our limited operating history; our ability to raise capital; our rate of growth; our ability to predict customer demand for our existing and future products; our ability to hire, retain and motivate employees; the effects of competition, including price competition; technological, regulatory and legal developments; and developments in the economy and financial markets.

We assume no obligation, and do not intend, to update these forward-looking statements, except as required by law.

Company Overview

Leading provider of MRAM technology and products for **mission-critical applications** in

- **Data Center**
- **Industrial**
- **IoT**
- **Automotive**
- **Radiation hardened applications**

20+
Years in production

200M+
MRAM units shipped

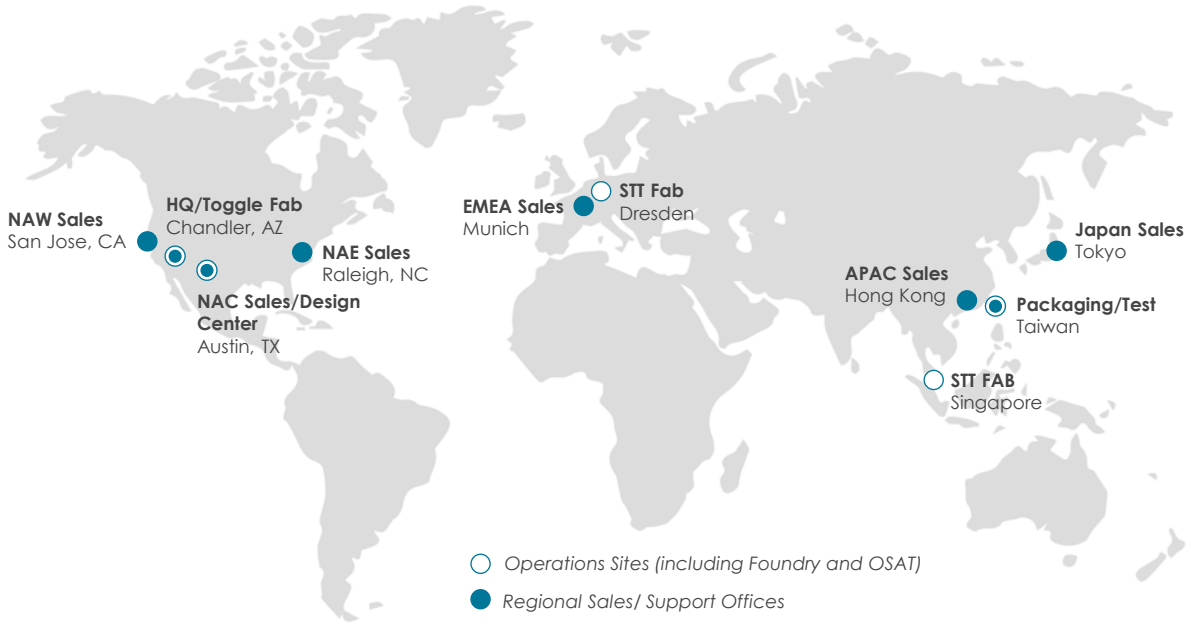
2,000+
Customers

700+
Patents & applications

FAB PARTNERSHIPS

NXP, GlobalFoundries™, TSMC, Microchip

GLOBAL OPERATIONS + SALES



Investment Highlights

Singular domestic provider of MRAM for mission critical applications

Diversified blue chip customer base across end markets and applications

Large market opportunity exceeding $4.3B by 2029

Proven management team with extensive experience delivering market leading technology solutions

Strong financial position with zero debt, expanding operating margins, and positive free cash flow

The MRAM Value Proposition

MRAM as a CPU-attached memory (like SRAM/DRAM) that brings non-volatile capability (like Flash)

Persistence

Maintains memory contents without requiring power

Endurance

Superior durability supports memory workloads without sophisticated management

Performance

SRAM & DRAM-like performance with low latency

Reliability

Best-in-class robustness designed and tested for extreme conditions

Everspin MRAM Products

System-in-Package Solutions

UNISYST in design / AgILYST in development

PERSYST

Persistent data memory

- Toggle-MRAM
- STT-DDRx
- STT-xSPI
  (EMxxLX family)

UNISYST

Unified code and data memory

- Enhanced Serial NOR-like
- LPDDRx Ultra-Fast Read
- Chiplet

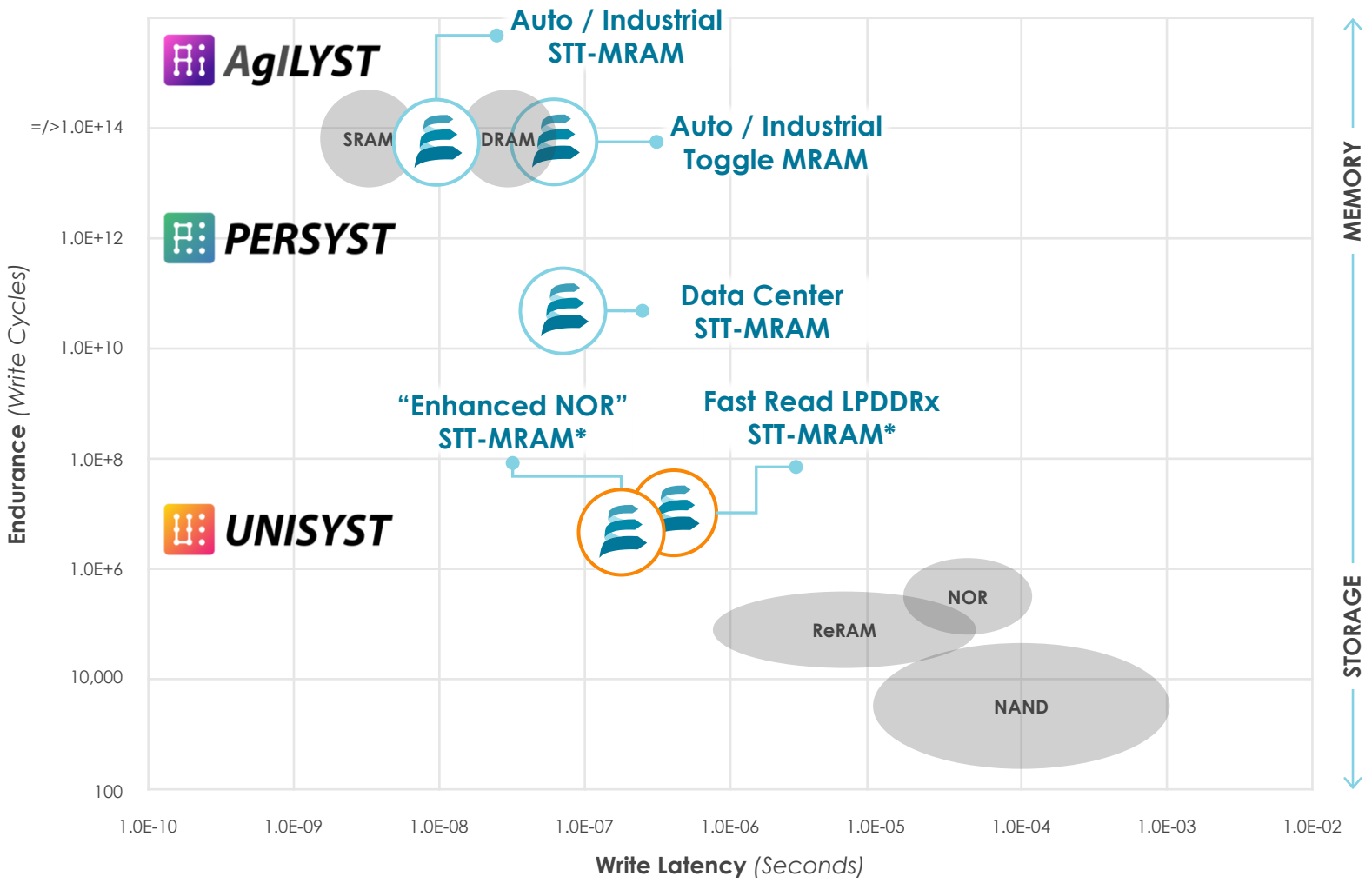
AgILYST

Innovation for transformation

- SRAM-like MRAM for FPGA configuration
- D-MRAM for AI Inferencing

Tunable MRAM For Varying Memory Workloads

SRAM (Data logging), DRAM (Data center) or NOR Flash (Configuration)



* In development

MRAM combines **performance of memory** with **persistence of storage**

Persistence

Decades of data retention without power or refresh

Performance Options

Read/write similar to DRAM and SRAM

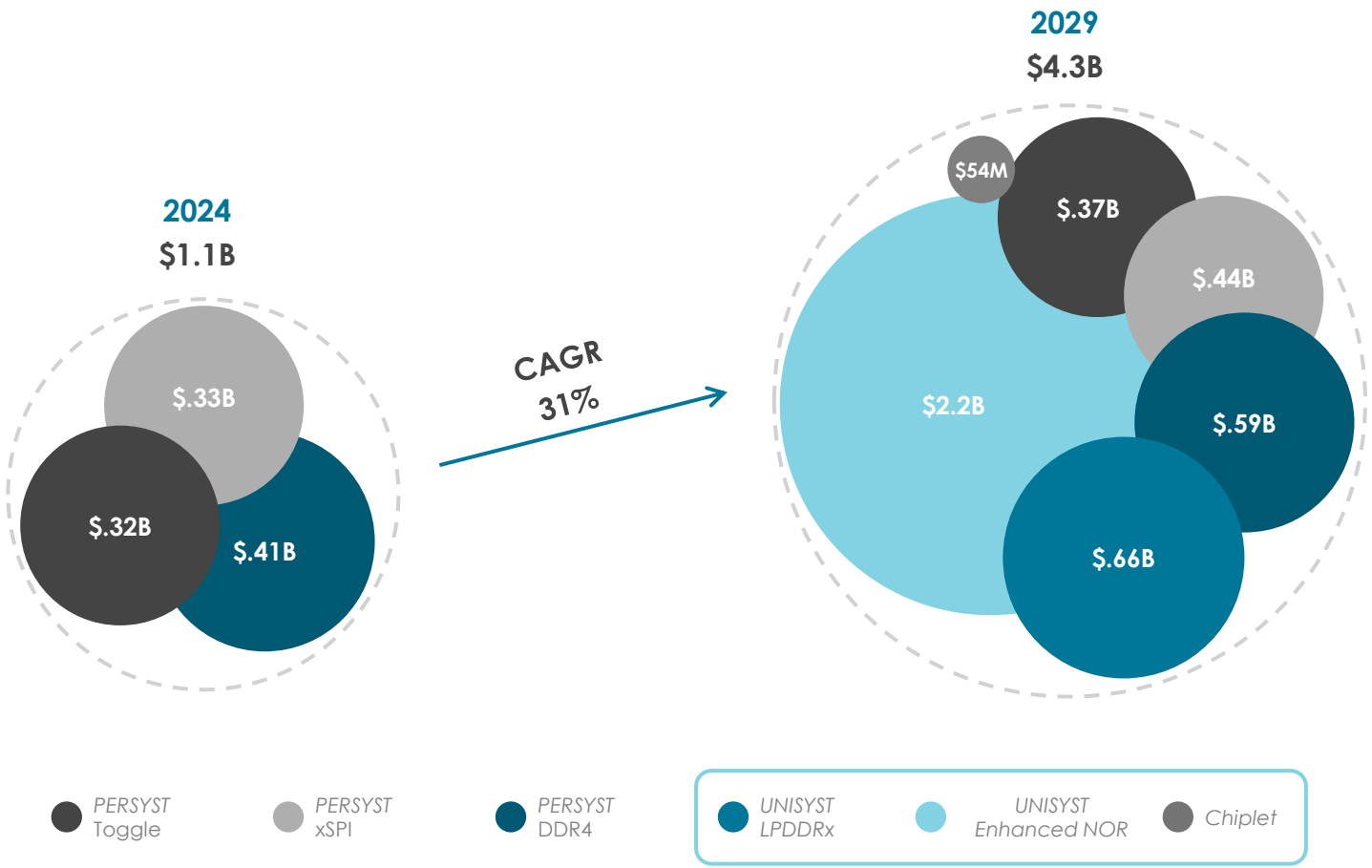
Enhanced NOR Page program

Endurance

Everspin optimizes MRAM for memory workloads

MRAM Product Roadmap Serves a Larger Market

PERSYST and *UNISYST* Served Market



PERSYST TAM

Legacy Toggle, ST-DDR4 and the new xSPI products serve a market of $1.1B in 2024

- Industrial
- Enterprise
- nvSRAM, FRAM

UNISYST TAM

Code and Data Unified products address NOR Flash and Embedded Compute

- Enhanced NOR withSPI Faster Writes
- LPDDRX- Faster Reads and Writes
- NVM Chiplets emerging

PERSYST Application Examples

Avionics & Transport

Data Recorders
Black box logging

Medical

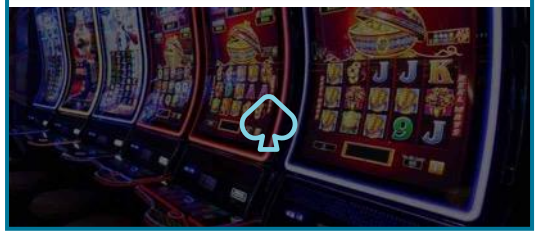
Monitoring Devices
Patient record logging

Automotive

Real Time Monitoring
Inverter and sensor logging

Gaming

Winning Validation
Casino, lottery, arcade, etc.



Aerospace

Space / Satellite
Code and data

Industrial Controls

PLC Modules
Data acquisition logging

Electrical & Power Grids

Remote Terminal Units
Power data monitoring

Electrical

Battery Charging Units
Battery health management

Value proposition → Low latency (fast data logging), reliability at extreme temperatures

Mission Critical Applications - Taking Off with Everspin MRAM

Automotive – Transportation

- Engine Control Unit
- Inverter Control
- Infotainment Configuration
- Train Applications

LEO Satellite

- Datalogging
- FPGA Code/Config

Defense Systems

- Navigation Control
- FPGA Programming
- Radar Tuning
- Image Sensor Interpolation

Aviation – Commercial & Military

- Engine Cockpit Display (HMI)
- Flight Control System
- Engine Parametric Monitor
- Event Recording

Personal Flight System

- eVTOL
- Flight Control System

Rocket / Missile Systems

- Flight Control
- Datalogging
- Radar Arrays

MRAM in Mission Critical Applications

- MRAM can be better suited for harsh environments compared with charge-based memories (DRAM, SRAM, and Flash)
  - High-radiation environments for space, defense, and telecommunications applications
  - High temperatures in the automotive applications (**LUCID, Bugatti, etc.**)
- STT-MRAM provides a promising scaling path for these applications

In a camera of NASA Mars 2020 rover, Perseverance

On its way to Jupiter as part of NASA's Lucy Mission

In power train system of Hypercar EV's

Diversified Top-Tier Customer Base in Significant Markets

Serving some of the most demanding customers in segments with long-term stability

Enterprise

- IBM
- DELL EMC
- BROADCOM
- MICROCHIP
- NetApp

Industrial Automation

- SIEMENS
- Schneider Electric
- MITSUBISHI
- JUKI
- OMRON
- ifm

Medical

- NIKKISO
- Canon CANON MEDICAL
- GE HealthCare

Network & Infrastructure

- NXP
- JAG
- CISCO
- LUTRON
- SUPERMICRO

Casino Gaming

- NOVOMATIC
- IGT
- Quixant
- KONAMI

MIL / Aero & Transportation

- AIRBUS
- Honeywell
- FRONTGRADE
- e2v
- BAE SYSTEMS
- plus D
- BOMBARDIER
- NORTHROP GRUMMAN

Everspin Capabilities

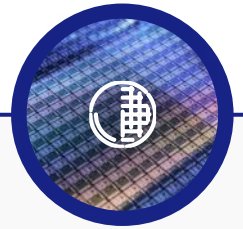
Everspin offers the widest capability to develop fully customized STT-MRAM → ***Design to manufacturing***

Design Services

- Discrete Memory Device or Embedded
- Custom STT-MRAM
- → **Full chip enablement or support in-house design team**
- Ability to leverage proven Everspin commercial IP
- Experience with multiple successful engagements

8" MRAM Manufacturing

- Everspin owned and operated 8" line in USA
- Any foundry CMOS
- → **In production / operation for over 20 years**
- Trusted US Gov programs and commercial
- Everspin Commercial Toggle MRAM line
- R&D capabilities *(Innovation)*



12" STT-MRAM 28/22/12nm

- ***PERSYST*** manufacturing partnership with GLOBALFOUNDRIES
- Manufactured fully at GF
- Commercially proven STT MRAM Line
- 22nm FD SOI available for rad hard designs

Everspin IP Delivers Value Through Licensing

TMR Sensors

ALPS

BOSCH

Mil-Aero Toggle-MRAM

Honeywell

FRONTGRADE

Embedded STT-MRAM

GlobalFoundries™

Head Sensors

SEAGATE

TDK Headway Technologies, Inc.

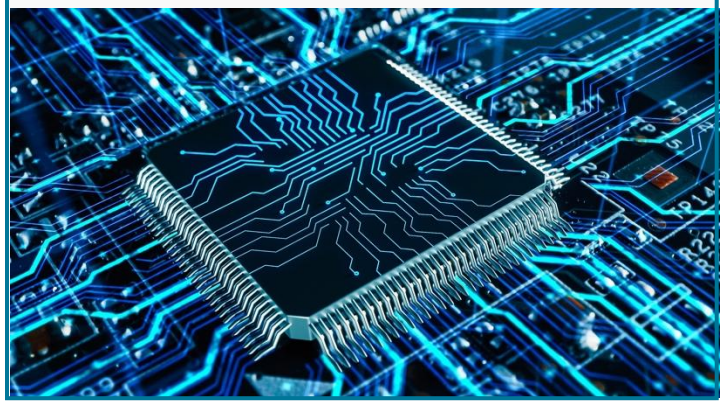
Everspin has successfully licensed MRAM, 3D Magnetic Sensor and TMR Head Sensor (HDD) IP

Embedded MRAM Licensee GLOBALFOUNDRIES expected to begin royalty bearing shipments in 2H 2020
Seagate TMR Head Sensor license in 2019 was based on a non-cash transfer of patents to Everspin

MRAM for NN, FPGA and Edge AI

MRAM for FPGA

- MRAM for configuration storage
- Chiplet for <40nm nodes where NOR does not scale
- → **SRAM-like MRAM for embedded architecture**

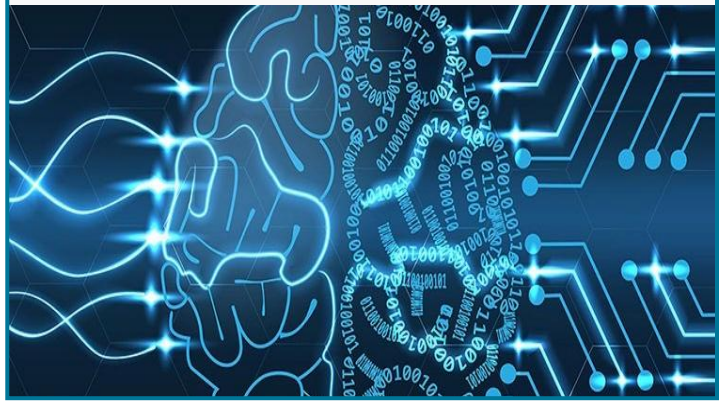


Distributed MRAM for AI

- → **Novel Everspin IP for "Distributed MRAM"**
- Instant on, Fast reads, Low power
- Distributed weight storage architecture for Edge

Synaptic Devices for NN

- Analog compute architecture
- → **Supervised and Unsupervised learning**
- Chiplet or Embedded architecture for Edge



Everspin roadmap addresses in-memory compute, FPGA configuration memory, and AI inference

MRAM & Technology Roadmap

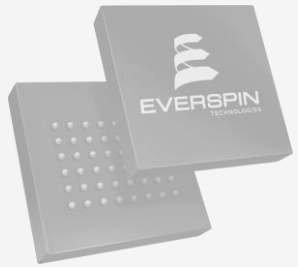
PERSYST
Persistent Data Memory

Toggle MRAM
P-SRAM to 32Mb
Parallel and Serial

EMD001G
1Gb P-DRAM

EMxxLX
8 – 128Mb
P-SRAM
xSPI 200Mhz

EMxxLX
4 – 16Mb
P-SRAM
xSPI 200Mhz



UNISYST
Unified Code and Data Memory

256Mb-2Gb
Fast Read
xSPI 200Mhz

256Mb-1Gb
Fast Read
LPDDRx

Co-packaging and Chiplet

System-in-Package
w/KGD

Chiplet
Wide IO

AgILYST
Distributed Memory for FPGA, Edge AI
(New Markets)

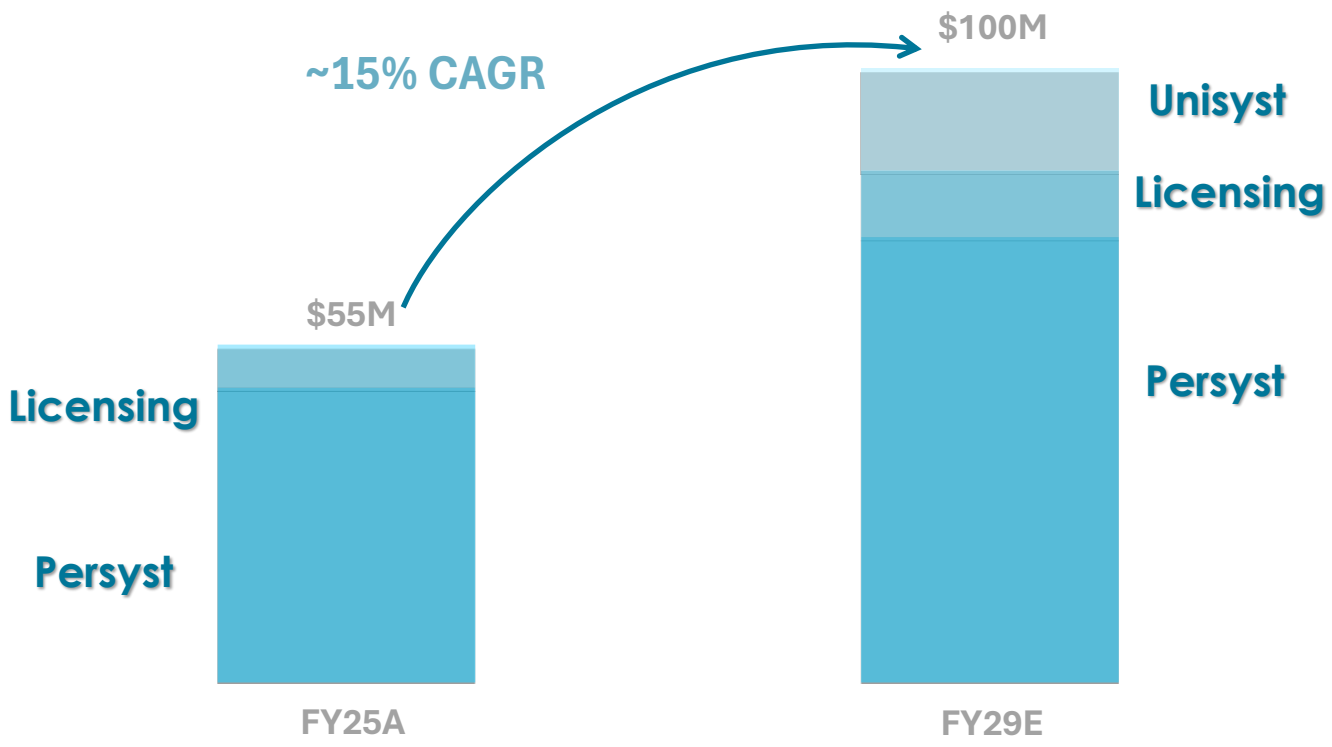
STT-MRAM
FPGA, Edge AI

STT-MRAM
FPGA, Edge AI

LEGACY **2023** **2024** **2025** **TODAY** **2027** **2028** →

8" legacy nodes *28nm* *8" 90nm* *New Products 16nm TSMC* *In Planning*

Path to $100M+ Revenue



PERSYST Family

- Toggle MRAM, ST-DDR MRAM and xSPI STT-MRAM
- Target $700M+ TAM
- Markets – Industrial, Casino, Data Center, LEO, EV's, A&D

Licensing & Other

- Licensing
- Foundry
- Royalties
- NRE

UNISYST Family

- High density xSPI STT-MRAM
- Target $3B+ TAM
- Markets – Enhanced NOR, FPGA, LEO, A&D

Executive Team

Proven team with strong experience in delivering market leading technology

Sanjeev Aggarwal
President & CEO
Texas Instruments

Bill Cooper
Chief Financial Officer
AMD

David Schrenk
VP, Business Development
intel

Amit Shah
VP, Backend Operations
Marvell, Semtech

Sean Dougherty
VP, Sales
altera, intel

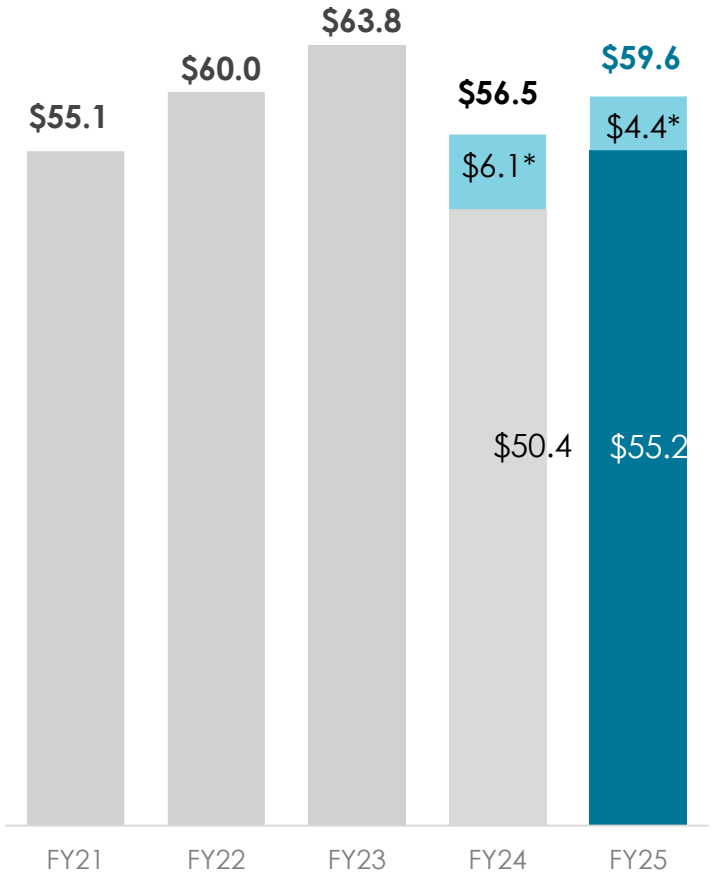
Yong Kim
VP, Product Development
Cypress

Kerry Nagel
VP, Technology R&D
motorola

Khaldoun Barakat
VP, FAB Operations & Quality
intel, Samsung

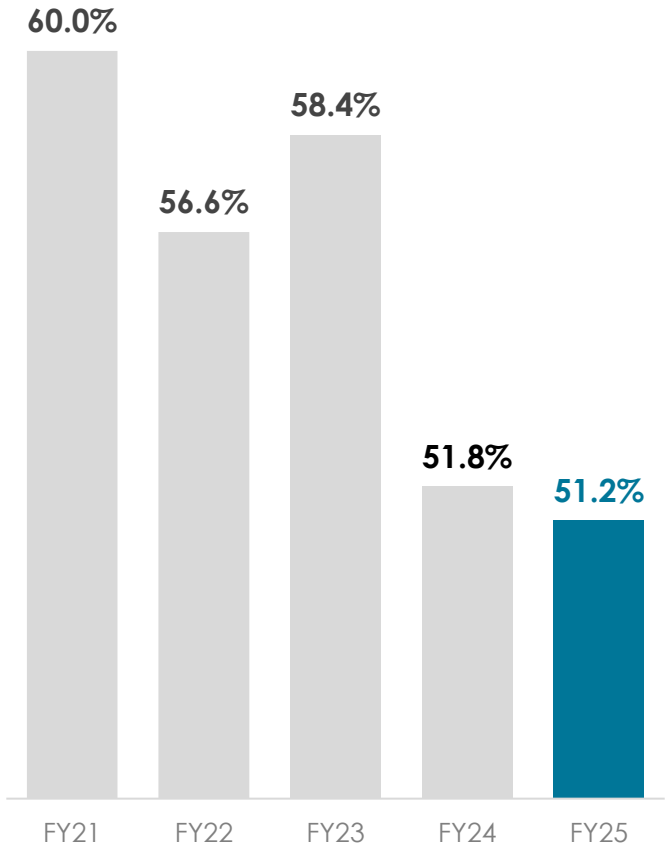
Financial Highlights

Revenue ($m)

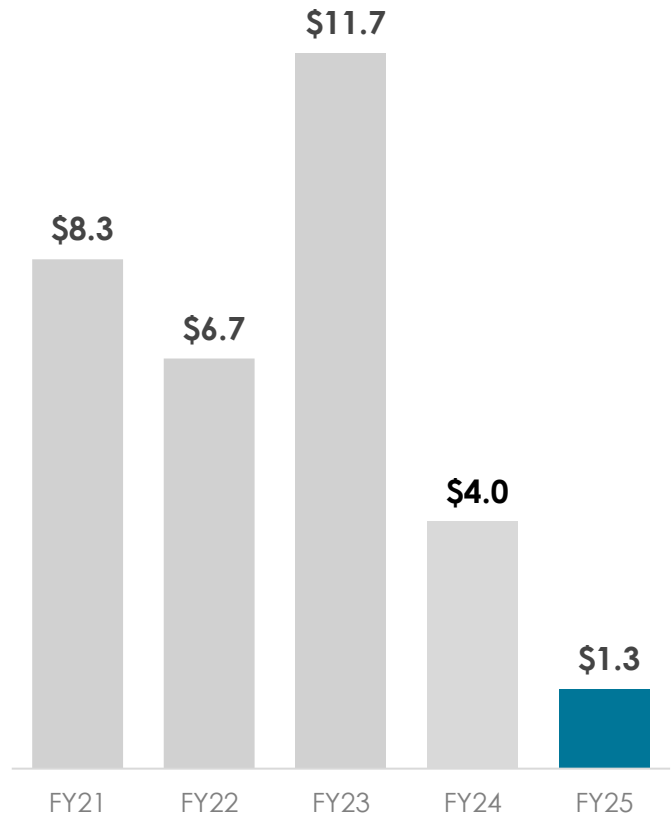


*Contract recognized in Other Income

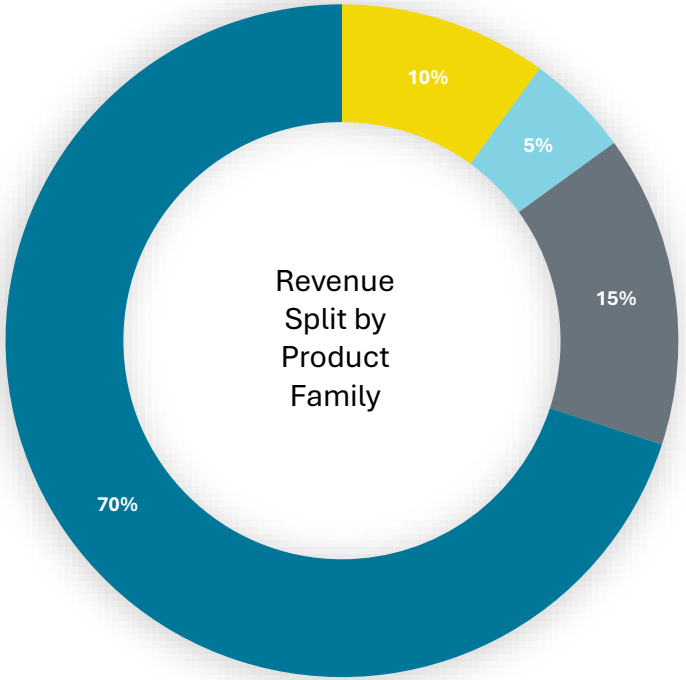
Gross Margin (%)



Free Cash Flow ($m)



Revenue Details – Product and Markets: Full Year 2025

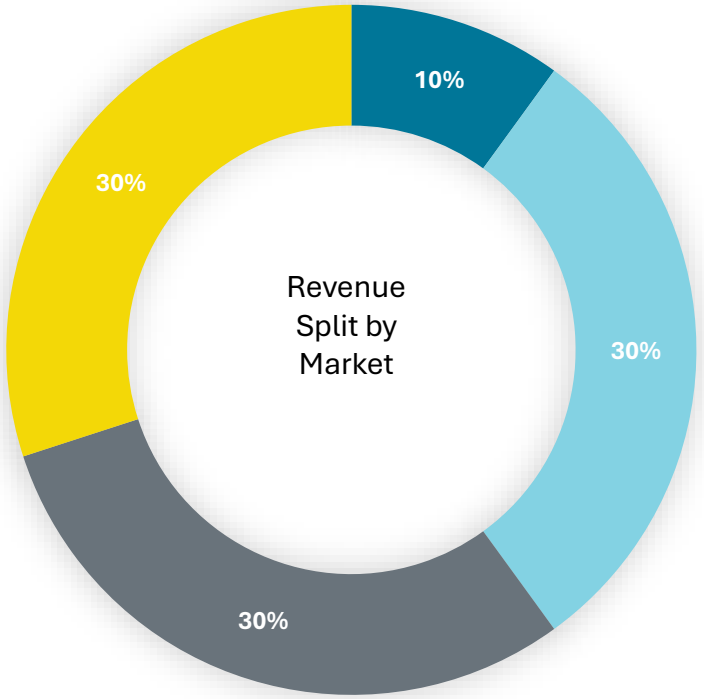


License

PERSYST - NOR (Toggle and STT-MRAM)

PERSYST - DDR

PERSYST - nVSRAM (Toggle and STT-MRAM)



LEO/Mil/Aero & Transportation

Enterprise

Industrial Automation

Casino Gaming

EVERSPIN®
TECHNOLOGIES


investor.everspin.com


ir@everspin.com